BC850C

NPN general purpose transistor

8 October 2024 Product data sheet

1. General description

NPN transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

PNP complement: BC860C

2. Features and benefits

- Low current (max. 100 mA)
- Low voltage (max. 45 V)

3. Applications

- General purpose switching and amplification

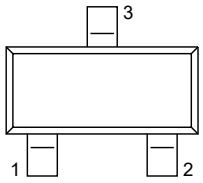
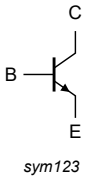
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	45	V
I_C	collector current			-	-	100	mA
h_{FE}	DC current gain	V_{CE} = 5 V; I_C = 10 µA; T_j = 25 °C		-	450	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base		
2	E	emitter		
3	C	collector		

SOT23

sym123

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6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BC850C	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BC850C	2G%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	50	V
V_{CEO}	collector-emitter voltage	open base		-	45	V
V_{EBO}	emitter-base voltage	open collector		-	5	V
I_C	collector current			-	100	mA
I_{CM}	peak collector current			-	200	mA
I_{BM}	peak base current			-	200	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	250	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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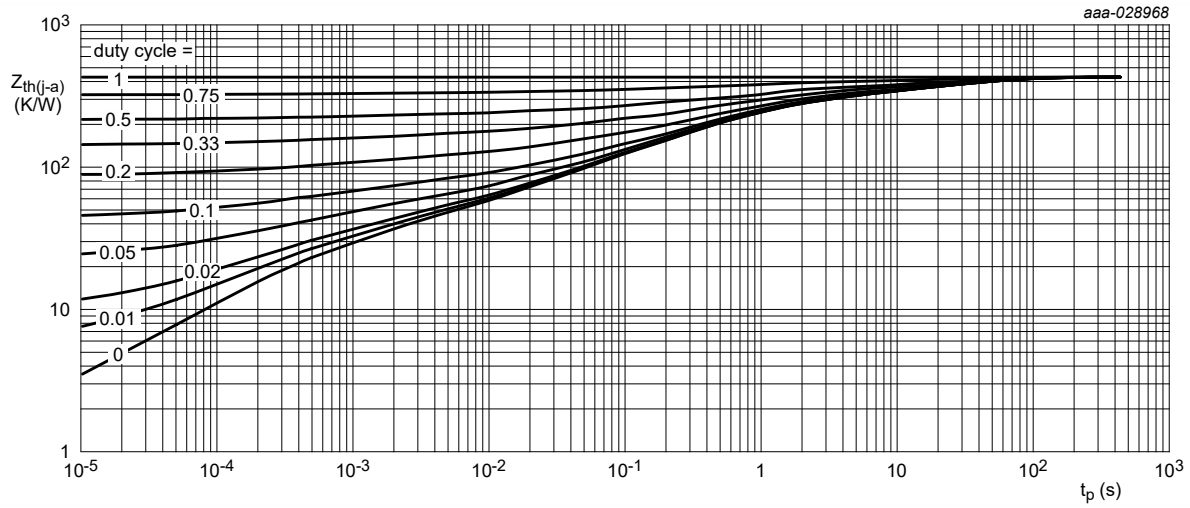

FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	V_{CB} = 30 V; I_E = 0 A; T_j = 25 °C		-	-	15	nA
		V_{CB} = 30 V; I_E = 0 A; T_j = 150 °C		-	-	5	µA
I_{EBO}	emitter-base cut-off current	V_{EB} = 5 V; I_C = 0 A; T_j = 25 °C		-	-	100	nA
h_{FE}	DC current gain	V_{CE} = 5 V; I_C = 10 µA; T_j = 25 °C		-	450	-	
		V_{CE} = 5 V; I_C = 2 mA; T_j = 25 °C		420	520	800	
V_{CEsat}	collector-emitter saturation voltage	I_C = 10 mA; I_B = 0.5 mA; T_j = 25 °C		-	90	250	mV
		I_C = 100 mA; I_B = 5 mA; T_j = 25 °C		-	200	600	mV
V_{BEsat}	base-emitter saturation voltage	I_C = 10 mA; I_B = 0.5 mA; T_j = 25 °C	[1]	-	700	-	mV
		I_C = 100 mA; I_B = 5 mA; T_j = 25 °C	[1]	-	900	-	mV
V_{BE}	base-emitter voltage	V_{CE} = 5 V; I_C = 2 mA; T_j = 25 °C	[2]	580	660	700	mV
		V_{CE} = 5 V; I_C = 10 mA; T_j = 25 °C	[2]	-	-	770	mV
C_c	collector capacitance	V_{CB} = 10 V; I_E = 0 A; i_e = 0 A; f = 1 MHz; T_j = 25 °C		-	2.5	-	pF
C_e	emitter capacitance	V_{EB} = 500 mV; I_C = 0 A; i_c = 0 A; f = 1 MHz; T_j = 25 °C		-	11	-	pF
f_T	transition frequency	V_{CE} = 5 V; I_C = 10 mA; f = 100 MHz; T_j = 25 °C		100	-	-	MHz
NF	noise figure	V_{CE} = 5 V; I_C = 200 µA; R_S = 2 kΩ; B = 200 Hz; f = 10 Hz to 15.7 kHz; T_j = 25 °C		-	-	4	dB
		V_{CE} = 5 V; I_C = 200 µA; R_S = 2 kΩ; f = 1 kHz; B = 200 Hz		-	-	4	dB

[1] V_{BEsat} decreases by about 1.7 mV/K with increasing temperature.
[2] V_{BE} decreases by about 2 mV/K with increasing temperature.

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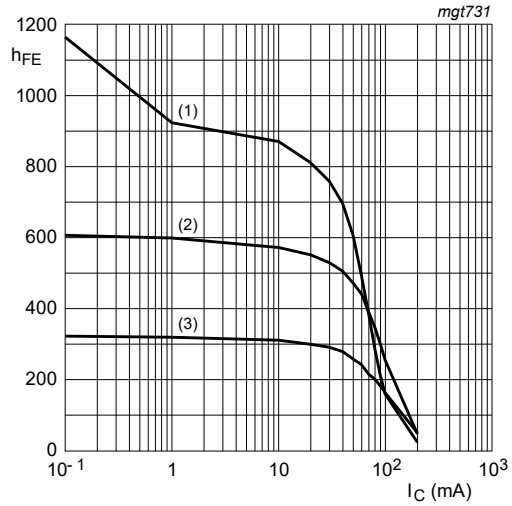
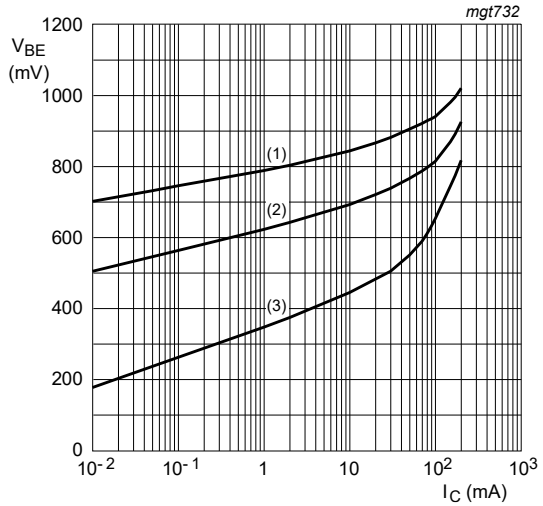


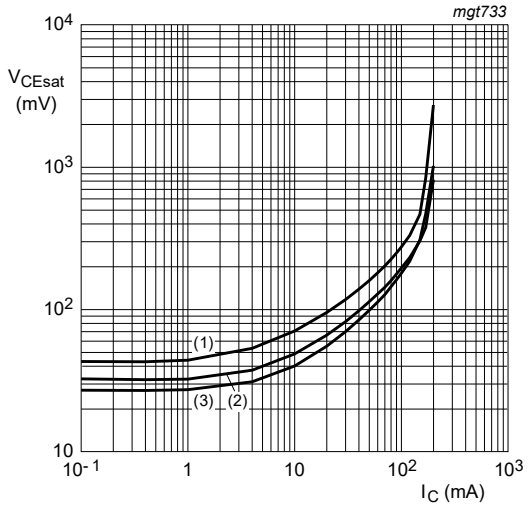
V_{CE} = 5 V
(1) T_{amb} = 150 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = -55 °C

Fig. 2. DC current gain as a function of collector current; typical values



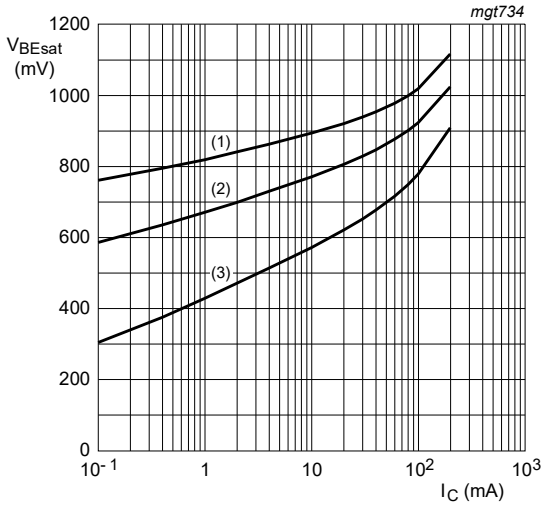
V_{CE} = 5 V
(1) T_{amb} = -55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 150 °C

Fig. 3. Base-emitter voltage as a function of collector current; typical values



I_C/I_B = 20
(1) T_{amb} = 150 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = -55 °C

Fig. 4. Collector-emitter saturation voltage as a function of collector current; typical values



I_C/I_B = 10
(1) T_{amb} = -55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 150 °C

Fig. 5. Base-emitter saturation voltage as a function of collector current; typical values

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11. Package outline

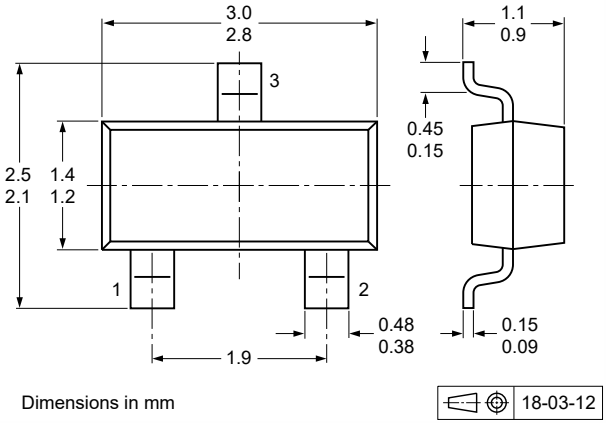

Fig. 6. Package outline SOT23

12. Soldering

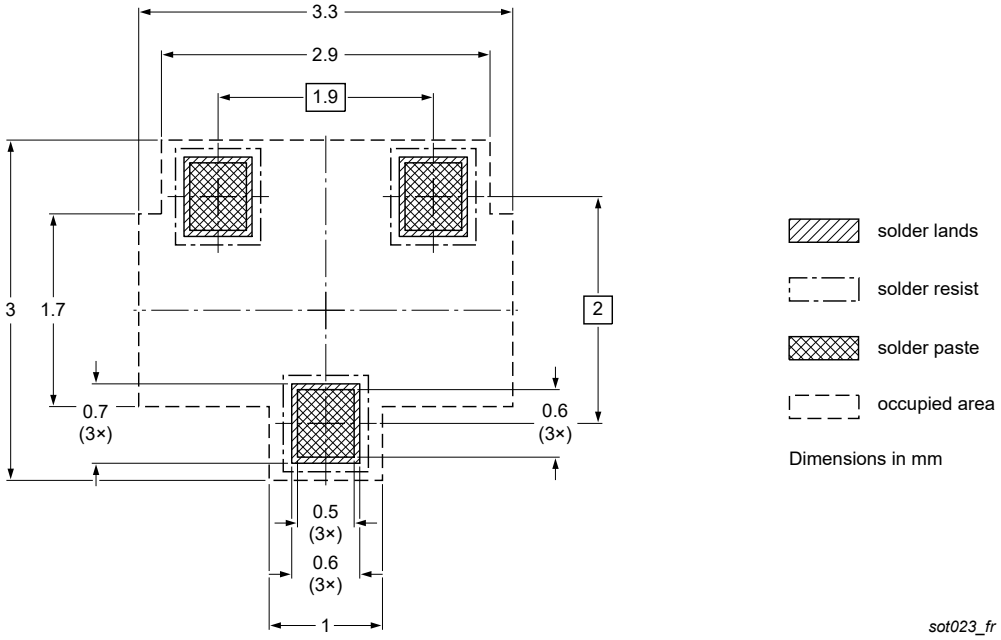

Fig. 7. Reflow soldering footprint for SOT23

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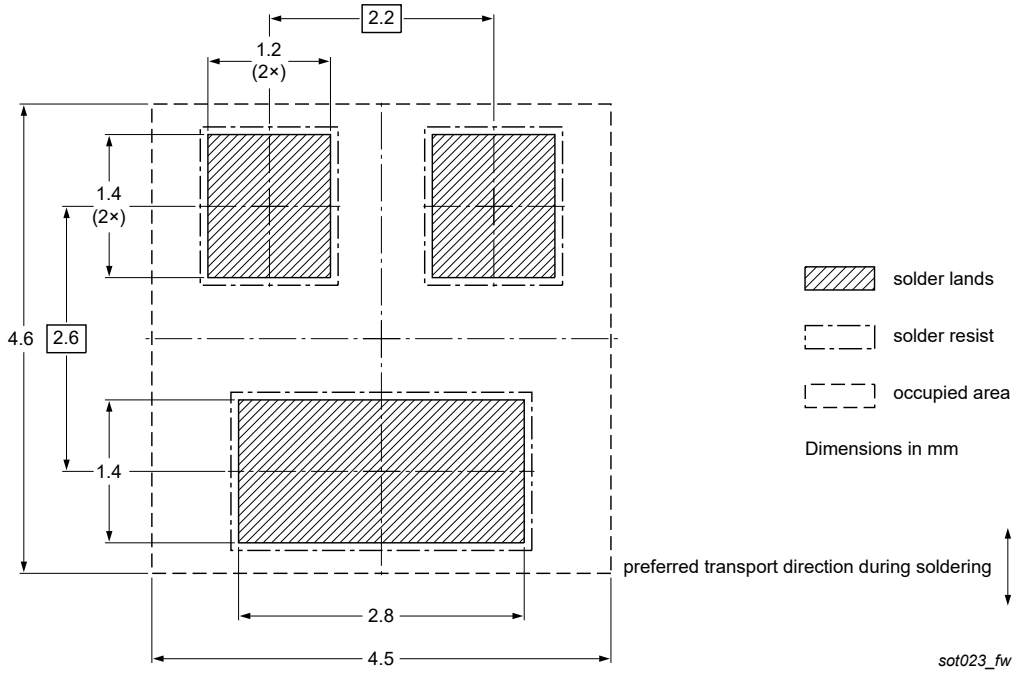

Fig. 8. Wave soldering footprint for SOT23

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13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC850C v.4	20241008	Product data sheet	-	BC850C v.3
Modifications:	• Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).			
BC850C v.3	20230425	Product data sheet	-	BC849_BC850 v.2
BC849_BC850 v.2	20040116	Product data sheet	-	BC849_BC850 v.1
BC849_BC850 v.1	19990408	Product data sheet	-	-

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14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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